WHAT IS CLAIMED IS:

1. A display processor comprising:

a first obtaining unit which obtains an average pixel value, or an average of pixel values in a predetermined area on a line;

an operation unit which calculates a pixel difference value, or a difference between the average pixel value and a pixel value of a target pixel to be corrected;

a processing unit which corrects the target pixel value, or the pixel value of the target pixel, according to the pixel difference value; and

a display unit which displays the pixel value corrected.

2. The display processor according to claim 1, wherein

the processing unit comprises:

a second obtaining unit which obtains a variation in pixel value near the target pixel; and

a correction unit which corrects the target pixel value according to the variation.

3. The display processor according to claim 2, wherein

the processing unit decreases the amount of correction of the target pixel value with an increasing variation, and increases the amount of correction of the target pixel value with a decreasing variation.

4. The display processor according to claim 2, wherein

the second obtaining unit obtains the variation based on adjoining-pixel difference absolute values, or absolute values of differences between the pixel values of pixels adjoining within a certain area near the target pixel.

5. The display processor according to claim 3, wherein

the second obtaining unit obtains the variation based on adjoining-pixel difference absolute values, or absolute values of differences between the pixel values of pixels adjoining within a certain area near the target pixel.

6. The display processor according to claim 4, wherein

the second obtaining unit obtains the variation based on an integrated value of the adjoining-pixel difference absolute values.

7. The display processor according to claim 5, wherein

the second obtaining unit obtains the variation based on an integrated value of the adjoining-pixel difference absolute values.

8. The display processor according to claim 4, wherein

if an adjoining-pixel difference absolute value exceeds a threshold, the second obtaining unit determines an integrated value by subjecting the threshold to the integration instead of the adjoining-pixel difference absolute value.

9. The display processor according to claim 5, wherein

if an adjoining-pixel difference absolute value exceeds a threshold, the second obtaining unit determines an integrated value by subjecting the threshold to the integration instead of the adjoining-pixel difference absolute value.

10. The display processor according to claim 2, wherein

the second obtaining unit compares each of the adjoining-pixel difference absolute values between adjoining pixels within a certain area near the target pixel with a threshold, and obtains the variation based on the counted number of adjoining-pixel difference absolute values exceeding the threshold.

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11. The display processor according to claim 3, wherein

the second obtaining unit compares each of the adjoining-pixel difference absolute values between adjoining pixels within a certain area near the target pixel with a threshold, and obtains the variation based on the counted number of adjoining-pixel difference absolute values exceeding the threshold.

12. The display processor according to claim 4, wherein

the second obtaining unit compares each of the adjoining-pixel difference absolute values between adjoining pixels within a certain area near the target pixel with a threshold, and obtains the variation based on the counted number of adjoining-pixel difference absolute values exceeding the threshold.

13. The display processor according to claim 5, wherein

the second obtaining unit compares each of the adjoining-pixel difference absolute values between adjoining pixels within a certain area near the target pixel with a threshold, and obtains the variation based on the counted number of adjoining-pixel difference absolute values exceeding the threshold.

14. The display processor according to claim 1, wherein

the processing unit corrects the target pixel value according to the position of the target pixel on the display unit.

15. The display processor according to claim 1, wherein

the first obtaining unit obtains the averages or integrated values of the pixel values in predetermined areas on a plurality of lines including the predetermined area on

the line,

the operation unit calculates a line difference value, or a difference between the average pixel values or integrated values of the lines, and

the processing unit corrects the target pixel value according to the line difference value.

16. The display processor according to claim 1, wherein

when the display unit is split into a plurality of areas for driving, the processing unit corrects the pixel value of a pixel at a position symmetrical to the target pixel in the split area.

17. An inorganic EL display processor comprising:

a first obtaining unit which obtains an average pixel value, or an average of pixel values in a predetermined area on a line;

an operation unit which calculates a pixel difference value, or a difference between the average pixel value and a pixel value of a target pixel to be corrected;

a processing unit which corrects the target pixel value, or the pixel value of the target pixel, according to the pixel difference value; and

a display unit which displays the pixel value corrected.

18. The inorganic EL display processor according to claim 17, wherein

the processing unit comprises:

a second obtaining unit which obtains a variation in pixel value near the target pixel; and

a correction unit which corrects the target pixel value according to the variation.

19. An organic EL A display processor comprising:

a first obtaining unit which obtains an average pixel value, or an average of pixel values in a predetermined area on a line;

an operation unit which calculates a pixel difference value, or a difference between the average pixel value and a pixel value of a target pixel to be corrected;

a processing unit which corrects the target pixel value, or the pixel value of the target pixel, according to the pixel difference value; and

a display unit which displays the pixel value corrected.

20. The organic EL display processor according to claim 19, wherein

the processing unit comprises:

a second obtaining unit which obtains a variation in pixel value near the target pixel; and

a correction unit which corrects the target pixel value according to the variation.